

APPARATUS AND METHOD FOR DYNAMICALLY DISABLING FAULTY
EMBEDDED MEMORY IN A GRAPHIC PROCESSING SYSTEM

ABSTRACT OF THE DISCLOSURE

A memory subsystem and method for ignoring faulty memory sub-arrays and assigning functional memory sub-arrays to accessible memory blocks. The memory subsystem includes a memory array segmented into a plurality of memory sub-arrays and a memory controller that accesses the memory array. The memory controller references a register storing a pointer value that assigns a memory block to each of the functional memory sub-arrays of the memory array. The faulty memory sub-arrays are left unassigned so that they are ignored by the memory controller. Upon receiving a memory access request to a particular memory block, the memory controller accesses the functional memory sub-array assigned to the particular memory block by the pointer value.

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